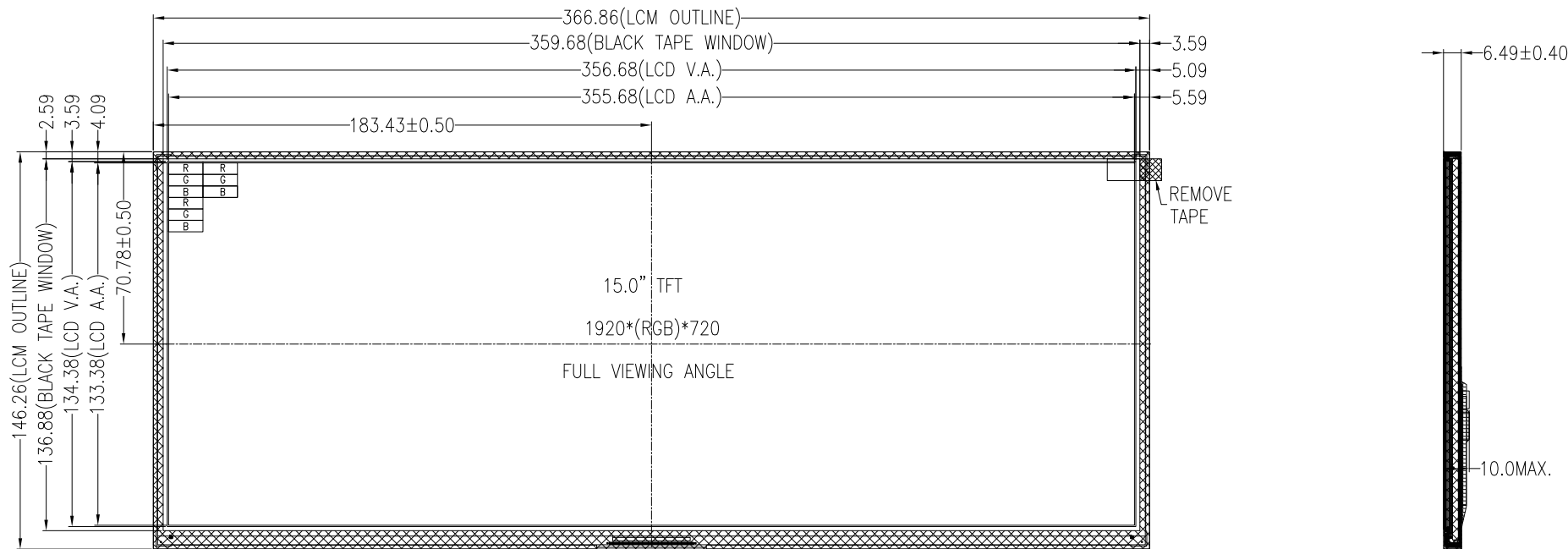
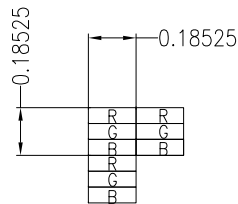


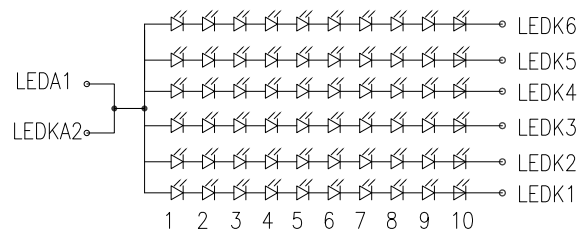


NOTES:

- 1.DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
- 2.OPERATING VOLTAGE: VCC=3.3V (TYP.)
- 3.VIEWING DIRECTION: FULL VIEWING ANGLE
- 4.DRIVER IC: HX82102-A
- 5.OPERATING TEMP.: -30°C ~ 85°C
- 6.STORAGE TEMP.: -40°C ~ 90°C
- 7.BACKLIGHT: IF=540MA, VF=27V~34V
- 8.SURFACE LUMINANCE: 1000CD/M² (TYP.)
- 9.TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. UNMARKED TOLERANCE: ±0.5MM
11. ROHS COMPLIANT



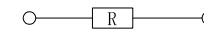
PIXEL DETAIL
(SCALE 80:1)



BACKLIGHT CIRCUIT DIAGRAM

MURATA: NCU15XH103F6SRC

THERMISTOR



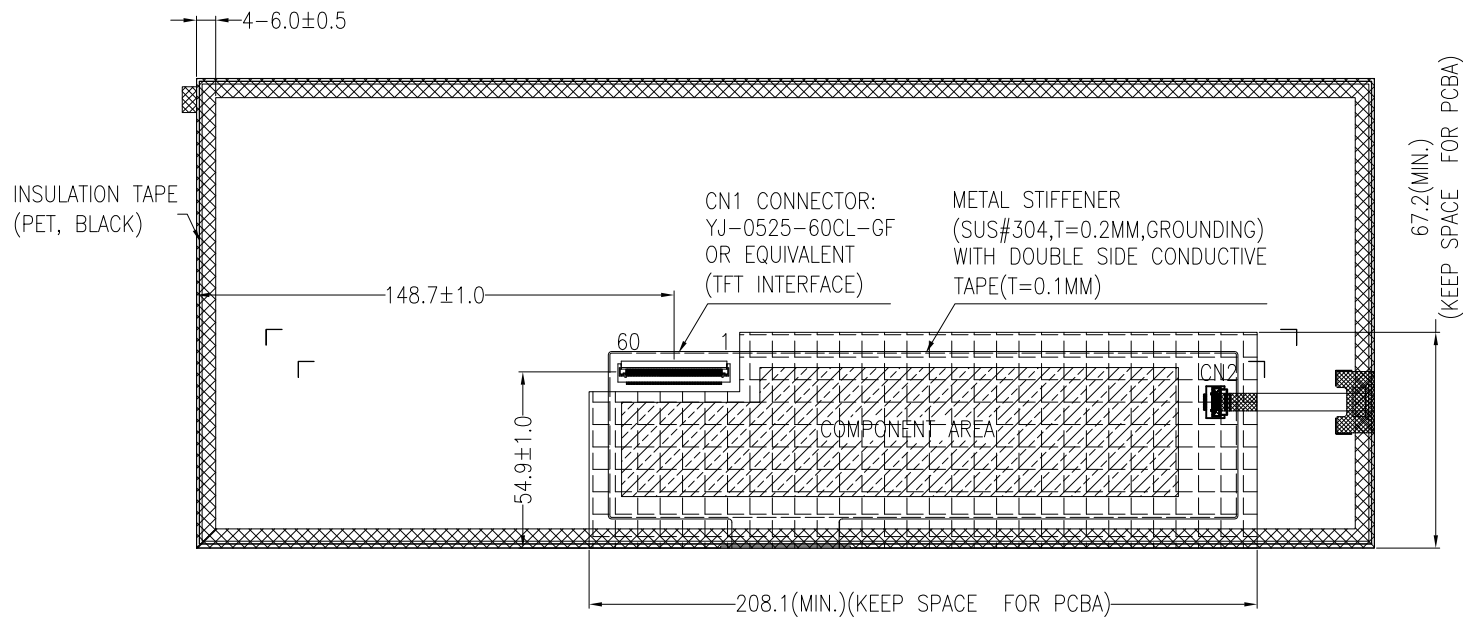
NTC CIRCUIT DIAGRAM



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2024.11.19		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI1500RT-2
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/2	TFT MODULE

01	FIRST ISSUE	RITA	2024.11.19
VER.	REVISED DESCRIPTION	REVISER	DATE



PIN ASSIGNMENT FOR CN1

PIN	SYMBOL	PIN	SYMBOL
1	LEDA1	31	GND
2	LEDA2	32	LVDS1_D1N
3	NTC+	33	LVDS1_D1P
4	NTC-	34	GND
5	LEDK1	35	LVDS1_D2N
6	LEDK2	36	LVDS1_D2P
7	LEDK3	37	GND
8	LEDK4	38	LVDS1_CLKN
9	LEDK5	39	LVDS1_CLKP
10	LEDK6	40	GND
11	VCC	41	LVDS1_D3N
12	VCC	42	LVDS1_D3P
13	GND	43	GND
14	LVDS0_D0N	44	IIC_SDA
15	LVDS0_D0P	45	IIC_SCL
16	GND	46	GND
17	LVDS0_D1N	47	RESET
18	LVDS0_D1P	48	FAIL_DET
19	GND	49	ATREN
20	LVDS0_D2N	50	VPP
21	LVDS0_D2P	51	STBYB
22	GND	52	GND
23	LVDS0_CLKN	53	NC
24	LVDS0_CLKP	54	NC
25	GND	55	NC
26	LVDS0_D3N	56	NC
27	LVDS0_D3P	57	NC
28	GND	58	NC
29	LVDS1_D0N	59	NC
30	LVDS1_D0P	60	NC



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2024.11.19		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI1500RT-2
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 2/2	TFT MODULE

01	FIRST ISSUE	RITA	2024.11.19
VER.	REVISED DESCRIPTION	REVISER	DATE